UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,103	03/24/2004	Koichi Matsuda	03500.017972.	2515
5514 7590 04/29/2008 FITZPATRICK CELLA HARPER & SCINTO 30 ROCKEFELLER PLAZA NEW YORK NY 10112			EXAMINER	
			HALL, ASHA J	
NEW YORK, NY 10112			ART UNIT	PAPER NUMBER
			1795	
			MAIL DATE	DELIVERY MODE
			04/29/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)
	10/807,103	MATSUDA ET AL.
Office Action Summary	Examiner	Art Unit
	ASHA HALL	1795
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D.  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period.  - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).
Status		
Responsive to communication(s) filed on 11 F      This action is <b>FINAL</b> . 2b) ☑ This      Since this application is in condition for allowed closed in accordance with the practice under the second se	s action is non-final. ance except for formal matters, pro	
Disposition of Claims		
4)  Claim(s) 1-8,12 and 13 is/are pending in the a 4a) Of the above claim(s) is/are withdra 5)  Claim(s) is/are allowed. 6)  Claim(s) 1-8,12 and 13 is/are rejected. 7)  Claim(s) is/are objected to. 8)  Claim(s) are subject to restriction and/o	awn from consideration.	
9) ☐ The specification is objected to by the Examine	er	
10) The drawing(s) filed on is/are: a) acceptable and any objection to the Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct should be a sh	cepted or b) objected to by the I drawing(s) be held in abeyance. See ction is required if the drawing(s) is object.	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documen 2. Certified copies of the priority documen 3. Copies of the certified copies of the priority documen application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applicationity documents have been receive nu (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal F 6) Other:	ate

Art Unit: 1795

## **DETAILED ACTION**

#### Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on February 11, 2008 has been entered.

### Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Nakajima et al. (Japanese Patent 2000-150934).

As to claim 1, Nakajima et al. show a stacked photovoltaic element (stacked photovoltaic device depicted in Figure 1) comprising a plurality of unit photovoltaic elements (photo-conversion layer, 3) each composed of a pin-junction (p-type semiconductor,3a) (i-type semiconductor layer, 3b) (n-type semiconductor layer, 3c), connected to each other in series (as shown in Figure 1). Nakajima et al. further discloes a zinc oxide layer/diffusion preventation layer (4) (paragraph 33) on top of the

Art Unit: 1795

photo-conversion layer (3) (Figure 1). Nakajima et al. discloses that the impurity concentration (which effects the resistivity –paragraph 37) of the zinc oxide/diffusion prevention layer (4) selective reflection layer varies in the direction of its thickness (paragraph 50-52).

Nakajima et al. further discloses wherein both surfaces of the zinc oxide layer are in contact with different unit photovoltaic elements as depicted by Figure 1 (i.e. the Examiner has interpreted the 'unit' as elements that comprise the photovoltaic device wherein on one side of the ZnO layer there is the reflective metal layer (5) on the other side of the ZnO layer is the n-type Si layer), and a resistivity/ the graded impurity concentration of zinc oxide layer (on the side near Si n-type layer (3)) on the surface in contact with a unit photovoltaic element near a substrate as seen from the zinc oxide layer is higher/higher impurity concentration (paragraph 8) than a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element farther from the substrate as seen from the zinc oxide layer. Resistivity is an characteristic property of the compositionally graded zinc oxide layer, the impurity concentration in the Zinc oxide layer of Nakajima et al. varies from one end to the opposite end of the compositionally graded zinc oxide layer, thus the claiming of a new use, new function or unknown property which is inherently present in the prior art does not necessarily make the claim patentable. In re Best, 562 F.2d 1252, 1254, 195 USPQ 430, 433 (CCPA 1977).

Art Unit: 1795

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sano et al. (US Patent 6,180,870 B1) in view of Ishihara et al. (US Patent 5,021,100) and in further view of Nakajima et al. (Japanese Patent Application Publication 2000-150934).

As to claim 1, Sano et al. show a stacked photovoltaic element (stack type photovoltaic device, depicted in Figure 1) comprising a plurality of unit photovoltaic elements (top cell, 150, middle cell, 140, and bottom cell, 130) each composed of a pinjunction ("pin structures", column 5, line 31), connected to each other in series (as shown in Figure 1). What Sano et al. fail to disclose is a zinc oxide layer provided at least one position between the unit photovoltaic elements.

Ishihara et al. disclose a stacked photovoltaic element ("multiple cell photovoltaic device", Figure 1) that contains an upper solar cell (second solar cell, 12) and a lower solar cell (first solar cell, 11) and that are separated by a selective reflection film (8). As Ishihara et al. explain in column 2, lines 42-47, the purpose of the selective reflective film is to reflect short wavelength light which may be absorbed by the upper solar cell (12) and to transmit long wave-length light which is not absorbed by the upper solar cell (12) and may be absorbed by the lower solar cell (first solar cell, 11). Ishihara et al. teach in column 2 lines 47-51 that this increases the photocurrent generated in the

Page 5

Art Unit: 1795

upper cell without reducing the current generated in the lower cell "thereby balancing the photocurrents in the respective cells." Ishihara et al. further teach that the material for the selective reflection layer may be zinc oxide (ZnO) in column 4, line 54. It would have been obvious to one of ordinary skill in the art at the time of the invention to provide the selective reflection layer of Ishihara et al. between each of the photovoltaic elements of Sano et al. (i.e., at the junction in which 150 is the upper cell and 140 is the lower cell, and at the junction in which 140 is the upper cell and 130 is the lower cell) in order to increase the photocurrent generated in the lower cell without reducing the current generated in the upper cell and thereby balance the photocurrents in the respective cells.

What the zinc oxide layer selective reflective layer of Ishihara et al. (and, therefore, the modified device of Sano et al) fails to provide is that the resistivity of the zinc oxide selective reflection layer varies in the direction of its thickness.

Nakajima et al. disclose photovoltaic cell (Drawing 1) that contains a zinc oxide diffusion barrier layer (4) with a non-uniform concentration of metallic impurities (e.g., 1-3 wt. % aluminum, paragraph 0013). As Nakajima et al. explain in paragraph 0004, the addition of metallic impurities such as aluminum or gallium to the zinc oxide layer raises the conductivity of this layer and leads to an improved junction between the zinc oxide layer (4) and subsequent photo-electric conversion layers (3) of the device. The concentration of metallic impurities in the zinc oxide layer is chosen to be non-uniform (i.e., greater at the end of the zinc oxide layer that contacts the photo-electric conversion layer than at the opposite end of the zinc oxide layer) so that the said

junction may be improved without compromising the light-transmissive properties of the zinc oxide layer (paragraph 0004). Nakajima et al. further teach that this non-uniform concentration may be either composed of "a plurality of layer constitutions having different impurity concentrations" or be a single layer with a "graded" impurity concentration (abstract). Any such graded concentration of metallic impurity along the direction of the thickness of a zinc oxide film would necessarily create a corresponding gradient in the resistivity of said film given that the presence of the metallic impurity increases the conductivity of the zinc oxide (paragraph 0004). It would have been obvious to one of ordinary skill in the art at the time of the invention to provide the nonuniform impurity concentration of Nakajima et al. to the zinc oxide selective reflection layers of the modified device of Sano et al. (so that the end of the zinc oxide film with the higher concentration of metallic impurity would be at the junction between the nlayer of 150 and the zinc oxide selective reflection layer separating 150 from 140, and at the junction between the n-layer of 140 and the zinc oxide selective reflection layer separating 140 from 130) in order to improve the junction between the zinc oxide layer and subsequent photo-electric conversion layers of the device without compromising the light-transmissive properties of the zinc oxide layer.

As to claim 2, applying the impurity concentration of Nakajima et al. to the modified device of Sano et al. as described above would necessarily create a zinc oxide layer with a lower resistivity on the side of the zinc oxide layer that is in contact with the n-layer of each of the upper photovoltaic devices (i.e., 140 and 150).

Art Unit: 1795

As to claim 3, applying the graded impurity concentration of Nakajima et al. to the modified device of Sano et al. as described above would necessarily create a continuous decrease in the resistivity of the zinc oxide from the side in contact with the p-layer of the upper photovoltaic device towards a side of the zinc oxide layer in contact with the n-layer of the lower photovoltaic device. This is so because the resistivity is inversely related to the concentration of metallic impurities which, as described above, would continuously increase in the zinc oxide layer from the side in contact with the p-layer of the upper photovoltaic device towards a side of the zinc oxide layer in contact with the n-layer of the lower photovoltaic device.

As to claims 4 and 5, one of ordinary skill in the art would tailor the concentration of metallic impurities in the graded impurity concentration of Nakajima et al. provided to the modified device of Sano et al. as needed in order to improve the junction between the zinc oxide layer and the subsequent photo-electric conversion layers of the device as instructed in by Nakajima et al. in paragraph 0005. Although Nakajima et al. do not report the sheet resistance of the layer, it has been shown that a few atomic % of Aldoping of zinc oxide films can give sheet resistances well within the range of claim 4 (i.e.,  $2x10^{\circ}$   $\Omega$ cm and  $5x10^{3}$   $\Omega$ cm) or claim 5 ( $5x10^{2}$   $\Omega$ cm and  $5x10^{3}$   $\Omega$ cm). For this see Figure 1 of Rabadanov et al. (R. A. Rabadanov, M. K. Guseikhanov, I. Sh. Aliev and S. A. Semiletov, "Properties of metal-zinc oxide contacts", Izvestiya Vysshikh Uchebnykh Zavedenii, Fizika, 6, 72–75 (1981)).

Art Unit: 1795

As to claim 6, Sano et al. state that the i-type layer (113) of the uppermost cell (150) may be composed of amorphous silicon in column 8 lines 6-8. Sano et al. further specify that said amorphous silicon may be a-Si-H in column 8 line 41.

As to claims 7 and 8, Sano et al. state the i-type layer (110) of the middle cell (140) is composed of microcrystalline silicon in column 8, line 9. Since microcrystalline silicon is a form of polycrystalline silicon, the same line indicates that the i-type layer of the middle cell may be composed of polycrystalline silicon.

6. Claims 12 and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Ishihara et al. (US Patent 5,021,100) in view of Mahan et al. (US 6, 468,885).

In regard to claim 12 and 13, Ishihara et al. discloses a method for producing a stacked photovoltaic element comprising an intermediate layer/ITO/selective reflective layer (8) between photovoltaic elements each having a pin-junction (3-5) (col.1; lines: 10-28 & col. 3; lines: 18-41 & col. 6; lines: 4-18) as shown in Figure 1, comprising the steps of: stacking a first layer(collectively photovoltaic-12 which is composed of 3-6) mainly composed of ITO (col. 3; lines:34-36) on at least one interface (6) with the photovoltaic element (12) as shown in Figure 1; and stacking a second layer/Photovoltaic (11) composed of (2, 1, 7, 8) mainly composed of zinc oxide (8) (Ishihara et al. also discloses other materials besides ITO for the selective reflection layer-8) (col. 4; lines: 50-57) (12) on direct contact the first layer of photovoltaic elements of p-i-n junction (11 or 12) and in direct contact to form a intermediate layer/middle layer/selective reflective layer (8). However, Ishihara et al. fails to disclose

Art Unit: 1795

that the second layer is formed at a higher rate than that of the first layer and also the second layer is formed at a higher temperature than the first layer.

Mahan et al. discloses deposition conditions for semiconductor device (col. 1; lines: 33-35) and teaches that the appropriate conditions for depositing silicon film and the hydrogen content of the film appears to be controlled by the temperature of the substrate and the deposition rate, such that the higher the temperature or the lower the deposition rate, the lower the hydrogen content (col. 4; lines: 34-49) and further teaches that the incorporation of hydrogen into the amorphous silicon layer which is usually accomplished during the deposition process it will considerably improve the electrical properties of the individual layers and the device(col. 1; lines: 40-45). It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate higher deposition techniques at lower temperatures as taught by Mahan et al. to the photovoltaic tandem cell of Ishihara et al. in order to control the hydrogen content thereby controlling the properties of the silicon layer of the cell.

# Response to Arguments

Claim Rejection 35 U.S.C 102(b)

As to claim 1, the Applicant argues that nothing in Nakajima et al. that would teach or suggest that "both surfaces of the zinc oxide layer are in contact with different unit photovoltaic elements, and a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element near a substrate as seen from the zinc oxide layer is higher than a resistivity of the zinc oxide layer on the surface in contact with a

Art Unit: 1795

unit photovoltaic element farther from the substrate as seen from the zinc oxide layer," as recited in Claim 1.

The Examiner respectfully disagrees. Nakajima et al. discloes a zinc oxide layer/diffusion preventation layer (4) (paragraph 33) on top of the photo-conversion layer (3) (Figure 1). Nakajima et al. discloses that the impurity concentration (which effects the resistivity –paragraph 37) of the zinc oxide/diffusion prevention layer (4) selective reflection layer varies in the direction of its thickness (paragraph 50-52).

Nakajima et al. further discloses wherein both surfaces of the zinc oxide layer are in contact with different unit photovoltaic elements as depicted by Figure 1 (i.e. the Examiner has interpreted the 'unit' as elements that comprise the photovoltaic device wherein on one side of the ZnO layer there is the reflective metal layer (5) on the other side of the ZnO layer is the n-type Si layer), and a resistivity/ the graded impurity concentration of zinc oxide layer (on the side near Si n-type layer (3)) on the surface in contact with a unit photovoltaic element near a substrate as seen from the zinc oxide layer is higher/higher impurity concentration (paragraph 8) than a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element farther from the substrate as seen from the zinc oxide layer. Resistivity is an characteristic property of the compositionally graded zinc oxide layer, the impurity concentration in the Zinc oxide layer of Nakajima et al. varies from one end to the opposite end of the compositionally graded zinc oxide layer, thus the claiming of a new use, new function or unknown property which is inherently present in the prior art does not necessarily make the claim patentable. In re Best, 562 F.2d 1252, 1254, 195 USPQ 430, 433 (CCPA 1977).

The Applicant further argues that Sano fails to teach or suggest a zinc oxide layer provided at least one position between the unit photovoltaic elements, much less a zinc oxide layer wherein "both surfaces of the zinc oxide layer are in contact with different unit photovoltaic elements, and a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element near a substrate as seen from the zinc oxide layer is higher than a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element farther from the substrate as seen from the zinc oxide layer," as recited in Claim 1 and Ishihara fails to remedy the deficiencies of Sano.

The Examiner respectfully disagrees. Claim 1 is unpatentable over Sano et al. (US Patent 6,180,870 B1) in view of Ishihara et al. (US Patent 5,021,100) and Nakajima et al. (Japanese Patent Application Publication 2000-150934) for the reasons as set forth above.

As for claim 12, the Applicant argues that Moreover, Applicants have found nothing in Ishihara that would teach or suggest "stacking a second layer mainly composed of zinc oxide on and in direct contact with the first layer to form the intermediate layer, wherein the second layer is formed at a rate higher than that of the first layer," as recited in Claim 12 (emphasis added).

The Examiner respectfully disagrees. Nakajima et al. discloes a zinc oxide layer/diffusion preventation layer (4) (paragraph 33) on top of the photo-conversion layer (3) (Figure 1). Nakajima et al. discloses that the impurity concentration (which

Art Unit: 1795

effects the resistivity –paragraph 37) of the zinc oxide/diffusion prevention layer (4) selective reflection layer varies in the direction of its thickness (paragraph 50-52).

Nakajima et al. further discloses wherein both surfaces of the zinc oxide layer are in contact with different unit photovoltaic elements as depicted by Figure 1 (i.e. the Examiner has interpreted the 'unit' as elements that comprise the photovoltaic device wherein on one side of the ZnO layer there is the reflective metal layer (5) on the other side of the ZnO layer is the n-type Si layer), and a resistivity/ the graded impurity concentration of zinc oxide layer (on the side near Si n-type layer (3)) on the surface in contact with a unit photovoltaic element near a substrate as seen from the zinc oxide layer is higher/higher impurity concentration (paragraph 8) than a resistivity of the zinc oxide layer on the surface in contact with a unit photovoltaic element farther from the substrate as seen from the zinc oxide layer. Resistivity is an characteristic property of the compositionally graded zinc oxide layer, the impurity concentration in the Zinc oxide layer of Nakajima et al. varies from one end to the opposite end of the compositionally graded zinc oxide layer, thus the claiming of a new use, new function or unknown property which is inherently present in the prior art does not necessarily make the claim patentable. In re Best, 562 F.2d 1252, 1254, 195 USPQ 430, 433 (CCPA 1977).

#### Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to ASHA HALL whose telephone number is (571)272-9812. The examiner can normally be reached on Monday-Thursday 8:30-7:00PM EST.

Art Unit: 1795

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alexa Neckel can be reached on 571-272-1446. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

AJH /A. H./ Examiner, Art Unit 1795

/Alexa D. Neckel/

Supervisory Patent Examiner, Art Unit 1795